


Title	Dr.	First Name	Manish	Last Name	Rai	
Designation		Asstt. Professor				
Department		Electronics And Communication Engineering				
Address (Campus)		Faculty of Engineering & Technology, MJP Rohilkhand University , Bareilly (UP) INDIA				
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<b>Education</b>						
Subject	Institution		Year	Details		
B.Sc.( Two Years)	Awadh University, Faizabad		1986, Ist Class Ist Position	P.C.M Group		
B. Tech	University of Allahabad ( A central University)		1990, Ist Class	Branch: Electronics And Telecommunication Engineering		
M. Tech	University of Allahabad ( A central University)		1993, Ist Class Ist Position	Branch: Electronics Engg.		
Ph.D	Moti Lal Nehru National Institute of Technology, University of Allahabad Allahabad.		2006	CDMA Techniques in Wireless Communication Networks.		
<b>Career Profile</b>						
Organization / Institution		Designation	Duration	Role		
Moti Lal Nehru regional Engg. College, University of Allahabad Allahabad.		Guest Lecturer	01 years	Teaching B.Tech / MCA Courses		
C.R State Engg. College, Sinepat(Haryana)		Guest Lecturer	01 years	Teaching B.Tech Course		
MJP Rohilkhand University, Bareilly(UP)		Asstt. Professor	14 year ( till date)	B.Tech/MCA Courses, Developed Analog and Digital Communication Labs, Circuit lab,CDMA Comm.		
Shri Mata Vaishno Devi University( A State University), Jammu.		Associate Professor	01/2 year	Teaching B.Tech/ M.Tech Courses		
<b>Research Interests / Specialization</b>						
CDMA Communication Techniques in Wireless Communication Networks, Spread Spectrum Communication, Broadband Communication, Digital & Analog Comm. etc.						
<b>Teaching Experience ( Subjects/Courses Taught)</b>						
<b>Total : 16 Years</b> Subjects: Wireless Communication including Digital and Analog Comm., CDMA Comm., Mobile Comm., VLSI Design, Satellite Communication, Optical Fiber Communication, Digital Electronics, Electronics Circuits & Design, Signal & Systems, Linear Integrated Circuits, Electromagnetic Wave Theory etc.						
<b>Honors &amp; Awards</b>						
National Merit Scholarship Holder from Class VIII to M.Tech level, Three Gold Medals at Graduate level including one from Governor.						
<b>Publications (LAST FIVE YEARS)</b>						
<b>Books / Monographs</b>						
<b><u>Year of Publication</u></b>	<b><u>Title</u></b>	<b><u>Publisher</u></b>	<b><u>Co-Author</u></b>			
No. Please						

<u>In Indexed/ Peer Reviewed Journals</u>			
<u>Year of Publication</u>	<u>Title</u>	<u>Journal</u>	<u>Co-Author</u>
2008	Improvement in Design of Hi-Lo Impedance Microstrip Low Pass Filter”	International Journals of Electrical and Electronics Engineering, ISSN 1392 – 1215, Lithuania, in 2008 Vol. No.7(87), P. 11-14.	Kumud Ranjan Jha
2008	Modification in microstrip low pass filter using bulb shape patch”	International Journal of Electronics and Communications, AEU ( <b>ELSEVIER</b> ), <b>Science Direct</b> on 21 August 2008.	Kumud Ranjan Jha
2008	Semicircular Microstrip Low Pass Filter”	Published in Asian Journal of Engineering and Applied Sciences- ISSN 1819-6608, Vol.3, No.4, August 2008	Kumud Ranjan Jha
<u>Articles</u>			
<u>Conference Presentations</u>			
International Conference: 12 including IEEE conference. National Conference : 02			
Total Publication Profile optional			
<u>Books</u>			
No. Please			
<u>Public Service / University Service / Consulting Activity</u>			
Member of Proctorial Board of University, Observer of Various Entrance Examinations conducted by the University, Member of University Cultural Committee for 2 years, Conducted Inter State University Technical Festival etc.			
<u>Professional Societies Memberships</u>			
IETE and ISTE member			
<u>Projects (Major Grants / Collaborations)</u>			
Submitted one major project to AICTE and one minor project in UGC.			
<u>Other Details</u>			
Completed M.Tech project work form R&D lab. of Uptron India Limited( a UP Govt. undertaking )			