


Title	Mr.	First Name	Yograj Singh	Last Name	Duksh	Photograph
Designation	READER					
Department	Electronics & Instrumentation Engineering					
Address (Campus)	Department of Electronics & Instrumentation Engineering, Faculty of Engineering & Technology, M.J.P. Rohilkhand University, Bareilly (U.P.)-243006 India					
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Education						
Qualification	Institution		Year		Details	
Ph.D. (VLSI-Design)	Shobhit University, Meerut		Pursuing		Topic: Crosstalk and Propagation Delay Analysis of Modern VLSI Interconnects	
M.Tech. (Instrumentation)	Regional Engineering College Kurukshetra		2001		Dissertation: A Study of Interconnect Parasitics in VLSI-chips	
M.Phil. (Instrumentation)	University of Roorkee Roorkee		1999		Dissertation: A Structural Tilt Measurement System	
M.Sc.	Rohilkhand University, Bareilly		1996		Physics with Electronics	
Career Profile						
Organisation / Institution		Designation		Duration		Role
M.J.P. Rohilkhand University, Bareilly		Reader		Sept 2008 to till date		Academic
Shobhit University, Meerut		Assistant Professor		Aug 2007 to Sept 2008		Academic
Shobhit Institute of Engg. Technology, Meerut		Sr. Lecturer		Aug 2006 to July 2007		Academic
Shobhit Institute of Engg. Technology, Meerut		Lecturer		Aug 2005 to July 2006		Academic
TMIMT, Moradabad		Lecturer		Feb 2002 to Aug 2005		Academic
Research Interests / Specialization						
VLSI-Design, VLSI Interconnects, Nanoelectronics, Nanotechnology, Measurement & Instrumentation Techniques.						
Teaching Experience (Subjects/Courses Taught)						
7 years & 6 months Digital Electronics, Automatic control system, Transducer, Digital Measurement Techniques, VLSI Technology, Digital VLSI Design, Digital System Design Using VHDL, Electronic Measurement & Instrumentation, Electronic Engineering Materials, Microprocessor 8085, 8086, BMI, Computer Organization.						
Honors & Awards						
Nil						
Publications (LAST FIVE YEARS)						

01
A Research Paper entitled “Performance Comparison of Carbon Nanotube, Nickel Silicide Nanowire and Copper VLSI Interconnects: Perspectives and Challenges Ahead” has been accepted in a International Journal “Journal of Engineering, Design & Technology” (JEDT), U.K.
Conference Presentations: 01
Total Publication Profile <i>optional</i>
Books : Nil
In Indexed/ Peer Reviewed Journals : Nil
Articles : Nil
Conference Presentations : 01
Public Service / University Service / Consulting Activity
1. Member, Board of Studies (Department of EI Engg.) 2. Project Coordinator, B.Tech. EI Branch 3. Member, Examination Committee of the University
Professional Societies Memberships
Not Applicable
Projects (Major Grants / Collaborations)
Not Applicable
Other Details (Training/workshop/conferences attended)
(1) Participated a Fourteenth “ NATIONAL POWER SYSTEM CONFERENCE-2006 ” held on 26 to 29 December, 2006 organized by Department of Electrical Engineering, IIT, Roorkee. (2) Participated a National Level Workshop on “ Current Trends in CAD Tools for VLSI Design ” held on 12 April, 2007 organized by CDAC, Noida (3) Attended the two weeks MHRD/AICTE Summer School on “ Design Techniques for VLSI & Nanotechnology (DTV-08) ” conducted by Department of Electronics & Communication Engineering, National Institute of Technology, Hamirpur (H.P.) between 12 to 21 July, 2008. (4) Attended a MHRD/AICTE summer School on “ VLSI Design & Optimization Techniques (VDOT-09) ” conducted by Department of Electronics & Communication Engineering, National Institute of Technology, Hamirpur (H.P.) between 06 to 10 July, 2009. (5) Attended a MHRD/AICTE summer School on “ Engineering Applications of EDA Tools using Verilog & SPICE (EDAT-09) ” conducted by Department of Electronics & Communication Engineering, National Institute of Technology, Hamirpur (H.P.) between 13 to 17 July, 2009